Near-Field Thermal Transistor

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Using a block of three separated solid elements, a thermal source and drain together with a gate made of an insulator-metal transition material exchanging near-field thermal radiation, we introduce a nanoscale analog of a field-effect transistor that is able to control the flow of heat exchanged by evanescent thermal photons between two bodies. By changing the gate temperature around its critical value, the heat flux exchanged between the hot body (source) and the cold body (drain) can be reversibly switched, amplified, and modulated by a tiny action on the gate. Such a device could find important applications in the domain of nanoscale thermal management and it opens up new perspectives concerning the development of contactless thermal circuits intended for information processing using the photon current rather than the electric current.

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The electronic solid-state transistor [Fig. 1(a)] introduced by Bardeen and Brattain in 1948 [1] is undoubtedly the cornerstone of almost all modern systems of information treatment. The classical field-effect transistor (FET), which is composed of three basic elements, the drain, the source, and the gate, is basically used to control the flux of electrons (the current) exchanged in the channel between the drain and the source by changing the voltage applied on the gate. The physical diameter of this channel is fixed, but its effective electrical diameter can be varied by the application of a voltage on the gate. A small change in this voltage can cause a large variation in the current from the source to the drain. In 2006, Li et al. [2] proposed a thermal counterpart of FET by replacing both the electric potentials and the electric currents by thermostats at a fixed temperature and heat fluxes carried by phonons through solid segments. Later, several prototypes of phononic thermal logic gates [3] as well as thermal memories (see [4] and references therein) were developed in order to process information by phonon heat flux rather than by electric currents. However, this technology suffers from some fundamental weaknesses which intrinsically limit its performance. One of the main limitations probably comes from the speed of acoustic phonons (heat carriers), which is 4 or 5 orders of magnitude smaller than the speed of photons. Another intrinsic limitation of phononic devices is related to the inevitable presence of local Kapitza resistances. Those resistances which come from the mismatch of vibrational modes at the interface of different elements can reduce the heat flux dramatically. Finally, the strong nonlinear phonon-phonon interaction mechanism makes the phononic devices difficult to deal with in the presence of a strong thermal gradient. On the contrary, the physics of transport mediated by photon tunneling remains unchanged close and far from thermal equilibrium. This explains, in part, why so many efforts have been deployed, during the past decades, to attempt to develop full optical or at least optoelectronic architectures for processing and managing information. In particular, important developments have been carried out during the past decade with plasmonics systems [5,6] to greatly increase the speed of information processing while reducing the dimension of devices at the nanoscale at the same time. However, despite these major steps forward, an optical transistor is still missing.

We introduce here a thermal transistor [Fig. 1(b)] based on the heat transport by evanescent photons rather than by acoustic waves or electrons. This near-field thermal transistor (NFTT) basically consists of a gate made of an insulator-metal transition (IMT) material which is able to qualitatively and quantitatively change its optical properties through a small change of its temperature around a critical temperature $T_c$. Vanadium dioxide ($\text{VO}_2$) is one of such materials (the choice of IMT depends on the operating range of the transistor: around $T = 500$ K, $\text{LaCoO}_3$ could replace $\text{VO}_2$, for instance) that undergoes a first-order transition (Mott transition [7]) from a high-temperature metallic phase to a low-temperature insulating phase [8] close to room temperature ($T_c = 340$ K). Different works have already shown [9–11] that the heat flux exchanged at close separation distances (i.e., in the near-field regime) between an IMT material and another medium can be modulated by several orders of magnitude across the phase transition of IMT materials. Further radiative thermal diodes have been recently conceived allowing for...
Rectification [12,13] of heat flux using materials with thermally dependent refractive indices [14–16] and IMT materials [17]. But so far controlling the heat flow with contactless systems to get the same functionalities as a classical FET has remained a challenging problem. Here we show that IMT materials are very promising candidates for designing efficient gates to control (switch, modulate, or amplify) the heat flux exchanged between two media.

To start, let us consider the system as illustrated in Fig. 1(b), where two media, which we call by analogy to a classical FET the source and the drain labeled by the indices S and D, are maintained at temperatures $T_S$ and $T_D$ with $T_S > T_D$ by some thermostats. A thin layer of IMT material labeled by $G$ having a thickness $\delta$ is placed between both media at a distance $d$ from the source and the drain. Without external excitation, the system reaches its steady state for which the net flux $\Phi_G$ received by the intermediate medium, the gate, is zero. In this case, its temperature $T_G$ is set by the temperature of the surrounding media, i.e., the drain and the source. When a certain amount of heat is added to or removed from the gate [for example, by applying a voltage difference through a couple of electrodes as illustrated in Fig. 1(b) or extracted from it using Peltier elements], its temperature can be either increased or reduced around its equilibrium temperature $T_G^{eq}$. Hence, the heat flux $\Phi_D$ received by the drain and the flux $\Phi_S$ lost by the source can be tailored accordingly. These fluxes correspond to the flux of the Poynting vector across any plane separating the gate and the drain (or the source and the gate). In a three-body system, the flux received by photon tunneling by the drain reads [18]

$$\Phi_D = \int_0^\infty \frac{d\omega}{2\pi} \varphi_D(\omega, d),$$

where the monochromatic heat flux is given by

$$\varphi_D = \hbar \omega \sum_{j=\{s,p\}} \int \frac{d^2 \mathbf{k}}{(2\pi)^2} \left[ n_{SG}(\omega) T_j^{S/G}(\omega, \mathbf{k}; d) + n_{GD}(\omega) T_j^{G/D}(\omega, \mathbf{k}; d) \right].$$

Here, $T_j^{S/G}$ and $T_j^{G/D}$ denote the efficiencies of the coupling of each mode $(\omega, \mathbf{k})$ between the source and the gate and between the gate and the drain for both polarization states $j = s$, $p$; $\mathbf{k} = (k_x, k_y)^t$ is the wave vector parallel to the surfaces of the multilayer system. In the above relation, $n_{ij}$ denotes the difference of Bose-distribution functions $n_i$ and $n_j$ [with $n_{ij} = 1/\exp(\hbar \omega/k_BT_{ij}) - 1$] at the frequency $\omega$; $k_B$ is Boltzmann’s constant and $2\pi\hbar$ is Planck’s constant.

According to the $N$-body near-field heat transfer theory presented in Ref. [18], the transmission coefficients $T_j^{S/G}$ and $T_j^{G/D}$ of the energy carried by each mode written in terms of optical reflection coefficients $\rho_{E,j}$ ($E = S, D, G$) and transmission coefficients $\tau_{E,j}$ of each basic element of the system and in terms of reflection coefficients $\rho_{EF,j}$ of couples of elementary elements are [18]

$T_j^{S/G}(\omega, \mathbf{k}, d) = \frac{4 |\tau_{G,j}|^2 \rho_{S,j} \rho_{D,j} e^{-2\gamma d}}{[1 - \rho_{SG,j} \rho_{GD,j} e^{-2\gamma d}]^2 [1 - \rho_{S,j} \rho_{DG,j} e^{-2\gamma d}]^2},$

$T_j^{G/D}(\omega, \mathbf{k}, d) = \frac{4 \rho_{S,G,j} \rho_{D,j} e^{-2\gamma d}}{[1 - \rho_{SG,j} \rho_{GD,j} e^{-2\gamma d}]^2}.$

Introducing the imaginary part of the wave vector normal to the surfaces in the multilayer structure $\gamma = \Im(k_c) = \sqrt{k^2 - \omega^2/c^2}$; $c$ is the velocity of light in vacuum. Similarly, the heat flux lost by the source reads

$$\Phi_S = \hbar \omega \sum_{j=\{s,p\}} \int \frac{d^2 \mathbf{k}}{(2\pi)^2} \left[ n_{DG}(\omega) T_j^{D/G}(\omega, \mathbf{k}; d) + n_{GS}(\omega) T_j^{G/S}(\omega, \mathbf{k}; d) \right],$$

where the transmission coefficients are analog to those defined in Eq. (3) and can be obtained making the substitution $S \leftrightarrow D$. 

**Fig. 1 (color online).** Electric and radiative thermal transistor. Classical field-effect transistor (a). A three-terminal device known as source, gate, and drain, which corresponds to the emitter, base, and collector of electrons. The gate is used to actively control, by applying a potential on it, the apparent conductivity of the channel between the source and the drain. Radiative thermal transistor (b). A layer of IMT material (the gate) is placed at subwavelength distances from two thermal reservoirs (the source and the drain). The temperatures $T_S$ and $T_D$ are fixed so that $T_S > T_D$. The temperature $T_G$ of the gate can be modulated from the external environment around its steady-state temperature $T_G^{eq}$ (which corresponds to the situation where the net flux $\Phi_G$ received by the gate vanishes) so that the flux $\Phi_D$ received by the drain and lost by the source $\Phi_S$ can be tuned. When $T_G^{eq}$ is a little bit smaller than the critical temperature $T_c$ of IMT material, a small amount of heat applied on the gate induces a strong switching of heat fluxes $\Phi_D$ and $\Phi_S$ owing to its metal-insulator phase transition.
At steady state, the net heat flux received or emitted by the gate vanishes. It is just given by the heat flux from the source to the gate minus the heat flux from the gate to the drain, i.e., in steady state $\Phi_S - \Phi_D = 0$.

This relation allows us to identify the gate equilibrium temperature $T_{G \text{eq}}$ (which is not necessarily unique) for given temperatures $T_S$ and $T_D$. Note that out of the steady state, the heat flux received or emitted by the gate is $\Phi_G = \Phi_S - \Phi_D \neq 0$. If $\Phi_G < 0$ ($\Phi_G > 0$), an external flux is added to (removed from) the gate by heating (cooling).

To illustrate the operating modes of NFTT, we now consider a system composed of a source and a drain both made of silica (each coupled to a thermostat to maintain their local temperatures constant in time) and a gate made of vanadium dioxide VO$_2$. When the gate temperature $T_G$ is smaller than its critical temperature $T_c$, then the gate is in its monoclinic phase and it behaves as a uniaxial crystal. On the other hand, when $T_G = T_c$, the gate transits toward its amorphous metallic phase and remains in this state for greater temperatures. Here we consider the case where the optical axis of VO$_2$ film is orthogonal to its interfaces. The $p$-polarized transmission coefficients of the energy carried by the modes ($\omega, \kappa$) through such a system are plotted in Fig. 2. When the gate is in its crystalline state, $T_{G/p}^S$ [see Fig. 2(a)] and $T_{G/D}^G$ [see Fig. 2(b)] show an efficient coupling of modes between the different blocks of the system around the resonance frequencies $\omega_{\text{SSP1}} \sim 1 \times 10^{14}$ rad/s and $\omega_{\text{SPP2}} \sim 2 \times 10^{14}$ rad/s of surface waves (surfaces phonon-polaritons) supported by both the source and the drain. Note that $T_{G/p}^S$ represents the exchange between the source and the drain mediated by the presence of the gate while $T_{G/D}^G$ corresponds to the exchange between the couple source-gate, treated as a unique body, and the drain. Below $T_c$ all parts of the system support surface waves in the same frequency range close to the thermal peak frequency $\omega_{\text{Wien}} \sim 1.3 \times 10^{14}$ rad/s. The anticrossing curves that appear in Figs. 2(a) and 2(b) result from the strong coupling of silica surface phonon polaritons (SPPs) and the surface waves (symmetric and anti-symmetric ones) supported by the thin VO$_2$ layer. Beyond $T_c$ the gate becomes amorphous (metallic) and it does not support surface waves anymore. In this case, $T_{G/p}^S$ [see Fig. 2(c)] vanishes owing to the field screening by the gate. Moreover, as is clearly shown in Fig. 2(d), the coupling of modes between the couple source gate and the drain at the frequency of the surface waves is less efficient for the large parallel values of $\kappa$, thus reducing the transfer of heat towards the drain; i.e., the number of participating modes decreases [20,21]. By using different physical parameters (temperatures, sizes, separation distances, etc.), several functions can be assigned to this system which can become either (i) a thermal switch, (ii) a thermal modulator, or (iii) a thermal amplifier. We discuss below those operating modes.

To do so, we show in Fig. 3 a plot of the net heat flux received by each part of the system in a particular configuration where $\delta = 50$ nm, $d = 100$ nm, $T_S = 360$ K, and $T_D = 300$ K. The thickness of the gate and the separation distances have been chosen in order to avoid size effects and to keep the near-field heat flux sufficiently

![FIG. 2 (color online). Transmission probabilities of energy carried by the modes. Efficiency of coupling of modes ($\omega, \kappa$) in a SiO$_2$-VO$_2$-SiO$_2$ system ($\delta = 50$ nm and $d = 100$ nm). (a) $T_{G}^{S/p}$ and (b) $T_{G/D}^{G}$ with VO$_2$ in its crystalline state. (c) $T_{G}^{S/p}$ and (d) $T_{G/D}^{G}$ with VO$_2$ in its amorphous state. Wien’s frequency (where the transfer is maximum) at $T = 340$ K is $\omega_{\text{Wien}} \sim 1.3 \times 10^{14}$ rad/s. The dielectric permittivity of SiO$_2$ is taken from the database [19].

![FIG. 3 (color online). Operating regimes of near-field thermal transistor. When $T_{G/p}^{S}$ is a little bit smaller than the critical temperature $T_c$ of the IMT material, a small amount of heat applied on the gate induces a strong switching of heat fluxes $\Phi_D$ and $\Phi_S$ owing to its phase transition. By changing the flux $\Phi_G$ supplied to the gate, different functions (thermal switching, thermal modulation, and thermal amplification) can be performed. The fluxes plotted here correspond to a gate of VO$_2$ [8] with thickness $\delta = 50$ nm located at a distance $d = 100$ nm from two massive silica samples [19] maintained at $T_S = 360$ K and $T_D = 300$ K.]}
large. Nevertheless, the presence of size effects could be taken into account without difficulty by using the appropriate optical properties.

**Thermal switching.**—In the situation depicted in Fig. (3), we have \( T_G = 332 \) K. An increase of \( T_G \) by about 10 deg (\( \Phi_G \) is increased, in absolute value, by \( \sim 10^{-8} \text{ W/}\mu\text{m}^2 \)) leads, as clearly shown in Fig. 3, to a reduction of heat flux received by the drain and lost by the source by more than 1 order of magnitude. That means our NFTT can be used in two operating modes where \( T_G \) is slightly below or above the critical temperature \( T_c \), where in the case of \( T_G < T_c \) we are in the “on” mode and for \( T_G > T_c \) we are in the “off” mode.

**Thermal modulation.**—Over the temperature region around \( T_G^{eq} \) (strip between the vertical lines in Fig. 3), the heat current \( \Phi_G \) over the gate remains quite small (i.e., \( \Phi_G \sim \Phi_D \)) while the flux received by the drain or lost by the source can be modulated from high to low values. The thermal inertia of the gate as well as its phase transition delay defines the time scale at which the modulator can operate. A much larger modulation of fluxes can be achieved with the NFTT when using \( T_G^{eq} \approx T_c \). Then the flux can be modulated over 1 order of magnitude by a small temperature change of \( T_G \).

**Thermal amplification.**—The most important feature for a transistor is its ability to amplify the current or electron flux towards the drain. In the region of the phase transition around \( T_c \), we see in Fig. 3 that an increase of \( T_G \) leads to a drastic reduction of flux received by the drain. This corresponds to a negative differential thermal conductance as recently described for SiC in Ref. [15] (note that this behavior does not violate the second principle of thermodynamics because the heat flux continues to flow from the hot to the cold body). Having a negative differential thermal conductance is the key for having an amplification, which is defined as (see, for example, Ref. [2])

\[
\alpha = \frac{\partial \Phi_D}{\partial \Phi_G} = \frac{1}{1 - \frac{\Phi'_D}{\Phi'_S}},
\]

where

\[
\Phi'_S/D \equiv \frac{\partial \Phi_S/D}{\partial T_G}.
\]

It can be easily seen from Fig. 3 that \( \alpha = 1/2 \) for \( T_G \) much smaller or larger than \( T_c \) where the material properties of \( \text{VO}_2 \) are more or less independent of \( T_G \), since \( \Phi_S' = -\Phi_D' \). On the other hand, inside the transition region of \( \text{VO}_2 \), that means for temperatures around \( T_c \), the material properties of \( \text{VO}_2 \) change drastically, showing a negative differential thermal resistance or conductance, which leads to an amplification, i.e., \( \alpha > 1 \). This behavior is qualitatively demonstrated in Fig. 4, where the dielectric permittivity of \( \text{VO}_2 \) in the transition region is modeled using either a Bruggeman mixing rule as introduced in Ref. [7] or the classical effective medium theory. Hence, by choosing the temperatures such that \( T_G^{eq} \approx T_c \), the NFTT works as an amplifier.

The ability to control the flow of heat at the subwavelength scale in complex architectures of solids out of contact opens up new opportunities for an active thermal management of dissipating systems. It also suggests the possibility to develop contactless thermal analogs of electronic devices, such as thermal logic gates and thermal memories, for processing information by utilizing thermal photons rather than electrons. Unlike other schemes for creating thermal transistors, which thus far have been based on the control of acoustic phonons, the present concept authorizes much higher operational speeds (speed of light) and should be very competitive compared to the previous ones. We think also that the near-field thermal transistors could find broad applications in MEMS or NEMS technologies where they could be used, for instance, to generate mechanical works by modulating the heat flux received by the drain, by using microresonators such as cantilevers in contact with it. Finally, a far-field version of this transistor could also be considered but with much weaker flux exchanged between the basic elements because of the natural limit set by the Stefan-Boltzmann law.

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